

2022 Master Class Series

NEW WAYS TO SHRINK

Extending Classic Moore's Law Scaling and Applying DTCO

April 21, 2022 at 9:00 AM PT / 12:00 PM ET

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REGINA FREED

Vice President
Alx™ Solutions

Regina Freed has more than 20 years of experience in the semiconductor industry, managing semiconductor process and equipment development for both logic and memory processes, including co-optimization between deposition and etch, lithography, metrology, and defect inspection. At Applied Materials, Regina leads our Alx™ program that enables us and our customers to accelerate development and ramp through Actionable Insights as well as control our processes to deliver improved process windows, enabling customers to scale faster and at lower cost, while optimizing device performance.

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OFER ADAN

Senior Director

Patterning Control Technology

Ofer Adan has more than 20 years of experience in metrology inspection and process control during which he has authored several patents, including the first patent on using high energy for SEM overlay. He has also authored more than 50 publications on metrology, inspection, process control, failure analysis, and fracture. He was awarded the SPIE Diana Nyssonen Memorial Award. He received an M.Sc. in electronic materials engineering from Ben Gurion University.

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UDAY MITRA

Vice President

Technology Roadmap and Strategy

Dr. Mitra has almost 4 decades of experience in the semiconductor industry managing technology integration, lithography, etch, thin films and packaging modules for both logic and memory products. Prior to joining Applied in 2005, he was director of technology integration at Intel Corporation, where he spent 17 years holding various management positions. Uday received his Ph.D. in Materials Science from MIT and is a recipient of the Henry Marion Howe Medal from the American Society of Materials International.

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RAMAN ACHUTHARAMAN

Group Vice President

Head of Technology, Strategy and Marketing

Dr. Raman Achutharaman leads the Semiconductor Products Group in defining the technology roadmap; developing the strategic plan to strengthen Applied's leadership in core wafer fabrication equipment markets; and communicating the vision, strategy, and success to stakeholders. Previously, as general manager of the Etch and Selective Products business unit, he led development and commercialization of the Sym3 Etch system that grew etch revenue from less than \$400M to over \$2B and gained more than 15 pts of conductor etch market share. Earlier, as general manager of the RTP group, he led development of the Vantage Radiance system, the basis of Applied's leadership in thermal processing. He received his undergraduate degree in metallurgical engineering from the Indian Institute of Technology, Chennai, and his Ph.D. in materials science and engineering from the University of Minnesota.

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MICHAEL SULLIVAN

Corporate Vice President
Investor Relations

Michael Sullivan is Corporate Vice President and head of Investor Relations. His team is responsible for investor relations and marketing communications including media relations, product and technology communications, and industry events. Mr. Sullivan joined Applied in 2009 after working at Intel Corporation for 16 years. He was Intel's primary interface to equity analysts and the company's largest institutional shareholders in the U.S. and Europe. He also held corporate communications positions at the company's U.S. and European offices, where he drove corporate and competitive initiatives, new microprocessor introductions, computing platform campaigns, and flash memory PR. Mr. Sullivan is past president of the Silicon Valley chapter of NIRI, the National Investor Relations Institute. He earned his MBA at Santa Clara University and his BA in Public Relations at San Jose State University.