



## Event Advisory: Leading Technologists to Explore Sub-22nm Flash Memory

December 2, 2009

SANTA CLARA, Calif.--(BUSINESS WIRE)--Dec. 2, 2009-- On December 8, in Baltimore, Maryland, Applied Materials will host an important symposium exploring critical questions surrounding the industry's capability to deliver the density and performance required to satisfy the ever-growing demand for higher capacity flash memory chips. Can conventional floating-gate technology remain viable beyond 22nm, or will three-dimensional charge trap or nanocrystal cell technology win out? What will these new structures look like and what new processes and materials will be needed to implement them?

Titled "How will NAND Flash Scale Beyond 2X?", the symposium will feature a stimulating panel discussion with leading technologists from across the flash memory industry. The panel will debate the challenges facing flash scaling and share their vision of how the industry landscape will evolve as we enter a new decade.

Panel: Yoshio Nishi, Stanford University, Moderator  
Hideaki Aochi – Toshiba Corp.  
Seiichi Aritome – Hynix Semiconductor, Inc.  
Siyoung Choi – Samsung Microelectronics, Ltd.  
Kirk Prall – Micron Technology, Inc.  
Hans Stork – Applied Materials

Where: Baltimore Marriott Inner Harbor at Camden Yards  
110 South Eutaw Street, Baltimore, Maryland 21201

When: Tuesday, December 8, 2009

Schedule: 5:15pm-6:15pm Registration and Reception  
6:15pm-7:30pm Panel Discussion  
7:30pm-8:00pm Networking

For more information on this exciting event, please visit: [http://www.appliedmaterials.com/sub22nm\\_panel/](http://www.appliedmaterials.com/sub22nm_panel/).

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