



Event Advisory: Applied Materials to Host Forum to Discuss Critical 32nm Patterning Challenges

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SANTA CLARA, Calif.--(BUSINESS WIRE)--Feb. 21, 2008--With 32nm technologies ramping within the next two years, implementing production-worthy techniques to address advanced patterning requirements becomes device makers' primary concern. On February 27th Applied Materials, Inc. will host a full day of technical presentations to discuss strategies and solutions to meet these scaling challenges. Experts will address critical patterning issues, from design automation to new process and inspection methodologies.

Dr. Hans Stork, chief technology officer of Applied's Silicon Systems Group, will lead this important forum, titled "Reality Takes Shape: Patterning at 32nm". The event will feature distinguished speakers from AMD, Cadence Design Systems, Freescale Semiconductor, Hynix, IBM, Qimonda, SEMATECH and Tower Semiconductor.

Topics:

- Self-aligned double patterning
- Immersion lithography
- OPC and electrical simulation modeling
- Etch for patterning beyond 32nm

Where: The Sainte Claire Hotel,
302 S Market St, San Jose, CA 95113

When: Wednesday, February 27th, 2008

Schedule:

8:30am-9:00am	Registration
9:00am-9:45am	Panel discussion
9:45am-11:45am	Morning seminar program
11:45am-12:45pm	Lunch
12:50pm-4:30pm	Afternoon seminar program
5:00pm-7:00pm	Reception

For more information on this exciting event, please visit www.appliedmaterials.com/2008_SPIE/.

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